

# **METHOD AND APPARATUS FOR PROVIDING WRITE PRE-COMPENSATION USING A READ TIMING PATH**

## **ABSTRACT**

5           A method and apparatus for providing write pre-compensation using a read timing path is disclosed. The present invention generates a first phase clock signal having a first phase and being synchronized with a read signal of a read path, generates a second phase clock signal having a second phase at a predetermined phase difference with the first clock signal and uses the first and second clock signals to shift write data to achieve write  
10   data comprising a first desired pre-compensation.